

**CAN controller interface****PCA82C250****FEATURES**

- Fully compatible with the "ISO/DIS 11898" standard
- High speed (up to 1 Mbaud)
- Bus lines protected against transients in an automotive environment
- Slope control to reduce radio frequency interference (RFI)
- Differential receiver with wide common-mode range for high immunity against electromagnetic interference (EMI)
- Thermally protected
- Short-circuit proof to battery and ground
- Low current standby mode
- An unpowered node does not disturb the bus lines
- At least 110 nodes can be connected.

**APPLICATIONS**

- High-speed applications (up to 1 Mbaud) in cars.

**GENERAL DESCRIPTION**

The PCA82C250 is the interface between the CAN protocol controller and the physical bus. The device provides differential transmit capability to the bus and differential receive capability to the CAN controller.

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CC}$	supply voltage		4.5	5.5	V
$I_{CC}$	supply current		–	170	$\mu$ A
$1/t_{bit}$	maximum transmission speed	non-return-to-zero	1	–	Mbaud
$V_{CAN}$	CANH, CANL input/output voltage		–8	+18	V
$\Delta V$	differential bus voltage		1.5	3.0	V
$t_{pd}$	propagation delay	high-speed mode	–	50	ns
$T_{amb}$	operating ambient temperature		–40	+125	$^{\circ}$ C

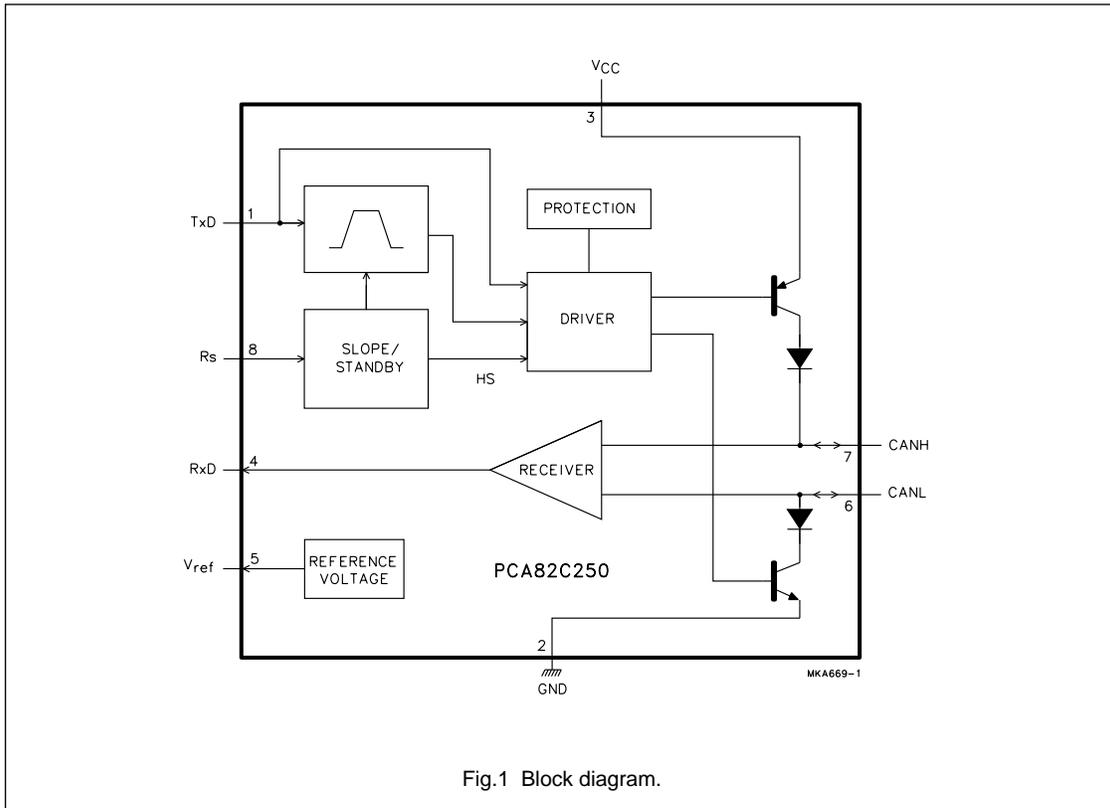
**ORDERING INFORMATION**

TYPE NUMBER	PACKAGE			
	PINS	PIN POSITION	MATERIAL	CODE
PCA82C250	8	DIP8	plastic	SOT97-1
PCA82C250T	8	SO8	plastic	SOT96-1

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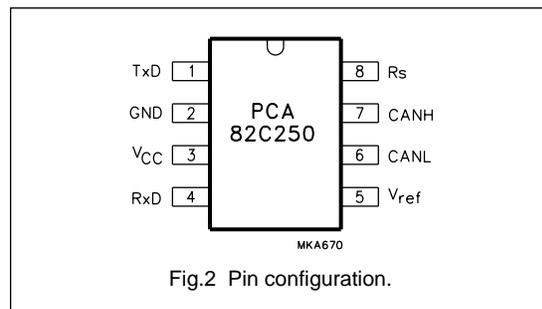
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BLOCK DIAGRAM



PINNING

SYMBOL	PIN	DESCRIPTION
TxD	1	transmit data input
GND	2	ground
V <sub>CC</sub>	3	supply voltage
RxD	4	receive data output
V <sub>ref</sub>	5	reference voltage output
CANL	6	LOW level CAN voltage input/output
CANH	7	HIGH level CAN voltage input/output
Rs	8	slope resistor input



## CAN controller interface

## PCA82C250

**FUNCTIONAL DESCRIPTION**

The PCA82C250 is the interface between the CAN protocol controller and the physical bus. It is primarily intended for high-speed applications (up to 1 Mbaud) in cars. The device provides differential transmit capability to the bus and differential receive capability to the CAN controller. It is fully compatible with the "ISO/DIS 11898" standard.

A current limiting circuit protects the transmitter output stage against short-circuit to positive and negative battery voltage. Although the power dissipation is increased during this fault condition, this feature will prevent destruction of the transmitter output stage.

If the junction temperature exceeds a value of approximately 160 °C, the limiting current of both transmitter outputs is decreased. Because the transmitter is responsible for the major part of the power dissipation, this will result in a reduced power dissipation and hence a lower chip temperature. All other parts of the IC will remain in operation. The thermal protection is particularly needed when a bus line is short-circuited.

The CANH and CANL lines are also protected against electrical transients which may occur in an automotive environment. Pin 8 (Rs) allows three different modes of operation to be selected: high-speed, slope control or standby.

For high-speed operation, the transmitter output transistors are simply switched on and off as fast as possible. In this mode, no measures are taken to limit the rise and fall slope. Use of a shielded cable is recommended to avoid RFI problems. The high-speed mode is selected by connecting pin 8 to ground.

For lower speeds or shorter bus length, an unshielded twisted pair or a parallel pair of wires can be used for the bus. To reduce RFI, the rise and fall slope should be limited. The rise and fall slope can be programmed with a resistor connected from pin 8 to ground. The slope is proportional to the current output at pin 8.

If a HIGH level is applied to pin 8, the circuit enters a low current standby mode. In this mode, the transmitter is switched off and the receiver is switched to a low current. If dominant bits are detected (differential bus voltage >0.9 V), RxD will be switched to a LOW level. The microcontroller should react to this condition by switching the transceiver back to normal operation (via pin 8). Because the receiver is slow in standby mode, the first message will be lost.

**Table 1** Truth table of CAN transceiver.

SUPPLY	TxD	CANH	CANL	BUS STATE	RxD
4.5 to 5.5 V	0	HIGH	LOW	dominant	0
4.5 to 5.5 V	1 (or floating)	floating	floating	recessive	1
<2 V (not powered)	X	floating	floating	recessive	X
2 V < V <sub>CC</sub> < 4.5 V	>0.75V <sub>CC</sub>	floating	floating	recessive	X
2 V < V <sub>CC</sub> < 4.5 V	X	floating if V <sub>Rs</sub> > 0.75V <sub>CC</sub>	floating if V <sub>Rs</sub> > 0.75V <sub>CC</sub>	recessive	X

**Table 2** Rs (pin 8) summary.

CONDITION FORCED AT Rs	MODE	RESULTING VOLTAGE OR CURRENT AT Rs
V <sub>Rs</sub> > 0.75V <sub>CC</sub>	standby	I <sub>Rs</sub> <  10 μA
-10 μA < I <sub>Rs</sub> < -200 μA	slope control	0.4V <sub>CC</sub> < V <sub>Rs</sub> < 0.6V <sub>CC</sub>
V <sub>Rs</sub> < 0.3V <sub>CC</sub>	high-speed	I <sub>Rs</sub> < -500 μA

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**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134). All voltages are referenced to pin 2; positive input current.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CC}$	supply voltage		-0.3	+9.0	V
$V_n$	DC voltage at pins 1, 4, 5 and 8		-0.3	$V_{CC} + 0.3$	V
$V_{6,7}$	DC voltage at pins 6 and 7	$0\text{ V} < V_{CC} < 5.5\text{ V}$ ; no time limit	-8.0	+18.0	V
$V_{trt}$	transient voltage at pins 6 and 7	see Fig.8	-150	+100	V
$T_{stg}$	storage temperature		-55	+150	°C
$T_{amb}$	operating ambient temperature		-40	+125	°C
$T_{vj}$	virtual junction temperature	note 1	-40	+150	°C

**Note**

- In accordance with "IEC 747-1".

An alternative definition of virtual junction temperature  $T_{vj}$  is:  $T_{vj} = T_{amb} + P_d \times R_{th\ vj-amb}$ ,  
where  $R_{th\ vj-amb}$  is a fixed value to be used for the calculation of  $T_{vj}$ .

The rating for  $T_{vj}$  limits the allowable combinations of power dissipation ( $P_d$ ) and ambient temperature ( $T_{amb}$ ).

**HANDLING**

Classification A: human body model; C = 100 pF; R = 1500  $\Omega$ ; V =  $\pm 2000\text{ V}$ .

Classification B: machine model; C = 200 pF; R = 0  $\Omega$ ; V =  $\pm 200\text{ V}$ .

**QUALITY SPECIFICATION**

Quality specification "SNW-FQ-611 part E" is applicable and can be found in the "Quality reference pocket-book" (ordering number 9398 510 34011).

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient in free air		
	PCA82C250	100	K/W
	PCA82C250T	160	K/W

## CAN controller interface

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**CHARACTERISTICS**

$V_{CC} = 4.5$  to  $5.5$  V;  $T_{amb} = -40$  to  $+125$  °C;  $R_L = 60$   $\Omega$ ;  $I_B > -10$   $\mu$ A; unless otherwise specified.

All voltages referenced to ground (pin 2); positive input current; all parameters are guaranteed over the ambient temperature range by design, but only 100% tested at  $+25$  °C.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Supply</b>						
$I_3$	supply current	dominant; $V_1 = 1$ V	–	–	70	mA
		recessive; $V_1 = 4$ V; $R_B = 47$ k $\Omega$	–	–	14	mA
		recessive; $V_1 = 4$ V; $V_B = 1$ V	–	–	18	mA
		standby; $T_{amb} < 90$ °C; note 1	–	100	170	$\mu$ A
<b>DC bus transmitter</b>						
$V_{IH}$	HIGH level input voltage	output recessive	$0.7V_{CC}$	–	$V_{CC} + 0.3$	V
$V_{IL}$	LOW level input voltage	output dominant	–0.3	–	$0.3V_{CC}$	V
$I_{IH}$	HIGH level input current	$V_1 = 4$ V	–200	–	+30	$\mu$ A
$I_{IL}$	LOW level input current	$V_1 = 1$ V	100	–	600	$\mu$ A
$V_{6,7}$	recessive bus voltage	$V_1 = 4$ V; no load	2.0	–	3.0	V
$I_{LO}$	off-state output leakage current	$-2$ V $< (V_6, V_7) < 7$ V	–2	–	+1	mA
		$-5$ V $< (V_6, V_7) < 18$ V	–5	–	+12	mA
$V_7$	CANH output voltage	$V_1 = 1$ V	2.75	–	4.5	V
$V_6$	CANL output voltage	$V_1 = 1$ V	0.5	–	2.25	V
$\Delta V_{6,7}$	difference between output voltage at pins 6 and 7	$V_1 = 1$ V	1.5	–	3.0	V
		$V_1 = 1$ V; $R_L = 45$ $\Omega$ ; $V_{CC} \geq 4.9$ V	1.5	–	–	V
		$V_1 = 4$ V; no load	–500	–	+50	mV
$I_{sc7}$	short-circuit CANH current	$V_7 = -5$ V; $V_{CC} \leq 5$ V	–	–	105	mA
		$V_7 = -5$ V; $V_{CC} = 5.5$ V	–	–	120	mA
$I_{sc6}$	short-circuit CANL current	$V_6 = 18$ V	–	–	160	mA
<b>DC bus receiver: <math>V_1 = 4</math> V; pins 6 and 7 externally driven; <math>-2</math> V <math>&lt; (V_6, V_7) &lt; 7</math> V; unless otherwise specified</b>						
$V_{diff(r)}$	differential input voltage (recessive)		–1.0	–	0.5	V
		$-7$ V $< (V_6, V_7) < 12$ V; not standby mode	–1.0	–	0.4	V
$V_{diff(d)}$	differential input voltage (dominant)		0.9	–	5.0	V
		$-7$ V $< (V_6, V_7) < 12$ V; not standby mode	1.0	–	5.0	V
$V_{diff(hys)}$	differential input hysteresis	see Fig.5	–	150	–	mV
$V_{OH}$	HIGH level output voltage (pin 4)	$I_4 = -100$ $\mu$ A	$0.8V_{CC}$	–	$V_{CC}$	V
$V_{OL}$	LOW level output voltage (pin 4)	$I_4 = 1$ mA	0	–	$0.2V_{CC}$	V
		$I_4 = 10$ mA	0	–	1.5	V
$R_i$	CANH, CANL input resistance		5	–	25	k $\Omega$

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{diff}$	differential input resistance		20	–	100	k $\Omega$
$C_i$	CANH, CANL input capacitance		–	–	20	pF
$C_{diff}$	differential input capacitance		–	–	10	pF
<b>Reference output</b>						
$V_{ref}$	reference output voltage	$V_8 = 1\text{ V};$ $-50\ \mu\text{A} < I_5 < 50\ \mu\text{A}$	$0.45V_{CC}$	–	$0.55V_{CC}$	V
		$V_8 = 4\text{ V};$ $-5\ \mu\text{A} < I_5 < 5\ \mu\text{A}$	$0.4V_{CC}$	–	$0.6V_{CC}$	V
<b>Timing (see Figs 4, 6 and 7)</b>						
$t_{bit}$	minimum bit time	$V_8 = 1\text{ V}$	–	–	1	$\mu\text{s}$
$t_{onTxD}$	delay TxD to bus active	$V_8 = 1\text{ V}$	–	–	50	ns
$t_{offTxD}$	delay TxD to bus inactive	$V_8 = 1\text{ V}$	–	40	80	ns
$t_{onRxD}$	delay TxD to receiver active	$V_8 = 1\text{ V}$	–	55	120	ns
$t_{offRxD}$	delay TxD to receiver inactive	$V_8 = 1\text{ V}; V_{CC} < 5.1\text{ V};$ $T_{amb} < +85\text{ }^\circ\text{C}$	–	82	150	ns
		$V_8 = 1\text{ V}; V_{CC} < 5.1\text{ V};$ $T_{amb} < +125\text{ }^\circ\text{C}$	–	82	170	ns
		$V_8 = 1\text{ V}; V_{CC} < 5.5\text{ V};$ $T_{amb} < +85\text{ }^\circ\text{C}$	–	90	170	ns
		$V_8 = 1\text{ V}; V_{CC} < 5.5\text{ V};$ $T_{amb} < +125\text{ }^\circ\text{C}$	–	90	190	ns
$t_{onRxD}$	delay TxD to receiver active	$R_8 = 47\text{ k}\Omega$	–	390	520	ns
		$R_8 = 24\text{ k}\Omega$	–	260	320	ns
$t_{offRxD}$	delay TxD to receiver inactive	$R_8 = 47\text{ k}\Omega$	–	260	450	ns
		$R_8 = 24\text{ k}\Omega$	–	210	320	ns
$ SR $	differential output voltage slew rate	$R_8 = 47\text{ k}\Omega$	–	14	–	V/ $\mu\text{s}$
$t_{WAKE}$	wake-up time from standby (via pin 8)		–	–	20	$\mu\text{s}$
$t_{dRxDL}$	bus dominant to RxD LOW	$V_8 = 4\text{ V};$ standby mode	–	–	3	$\mu\text{s}$
<b>Standby/slope control (pin 8)</b>						
$V_8$	input voltage for high-speed		–	–	$0.3V_{CC}$	V
$I_8$	input current for high-speed	$V_8 = 0\text{ V}$	–	–	–500	$\mu\text{A}$
$V_{stb}$	input voltage for standby mode		$0.75V_{CC}$	–	–	V
$I_{slope}$	slope control mode current		–10	–	–200	$\mu\text{A}$
$V_{slope}$	slope control mode voltage		$0.4V_{CC}$	–	$0.6V_{CC}$	V

**Note**

- $I_1 = I_4 = I_5 = 0\text{ mA}; 0\text{ V} < V_6 < V_{CC}; 0\text{ V} < V_7 < V_{CC}; V_8 = V_{CC}.$

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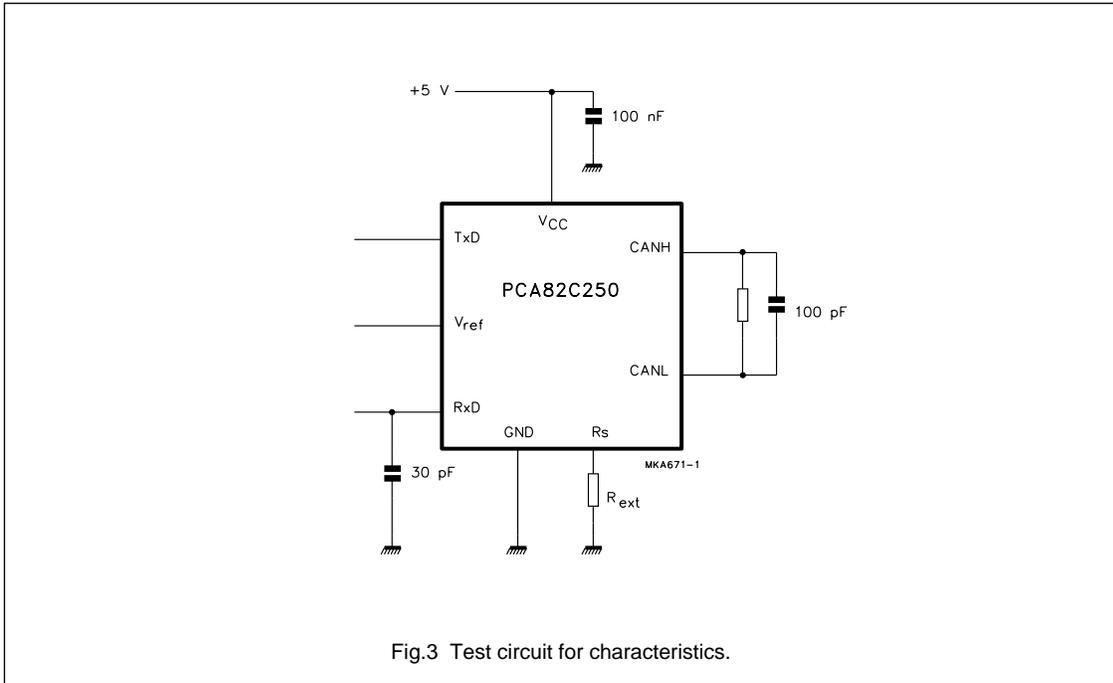


Fig.3 Test circuit for characteristics.

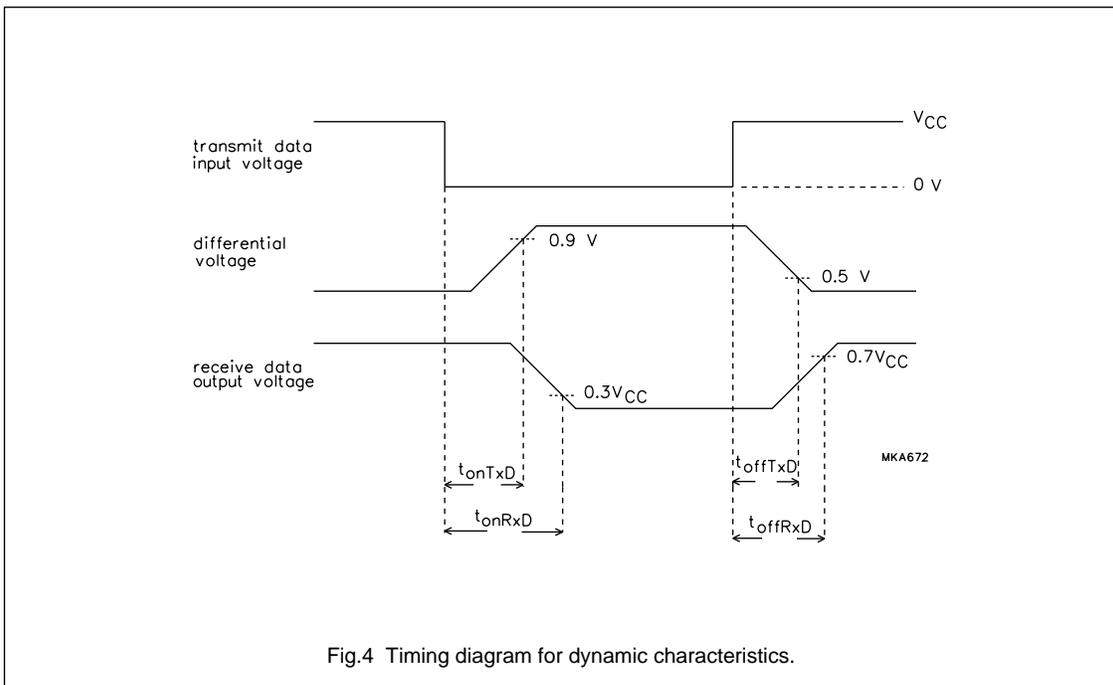


Fig.4 Timing diagram for dynamic characteristics.

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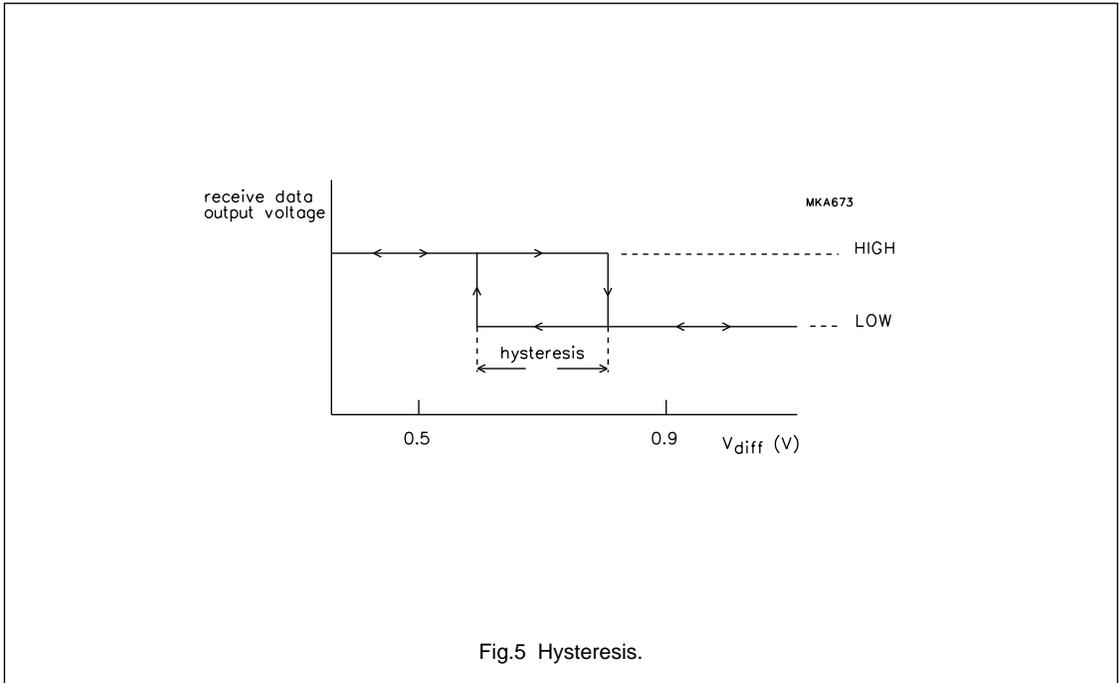


Fig.5 Hysteresis.

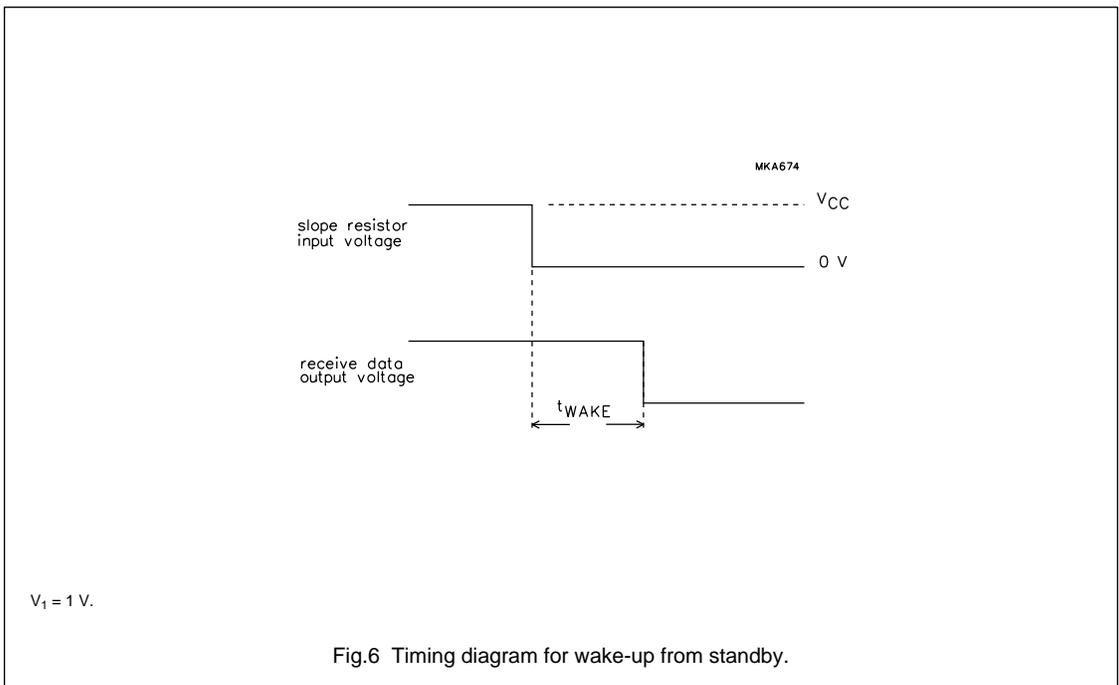
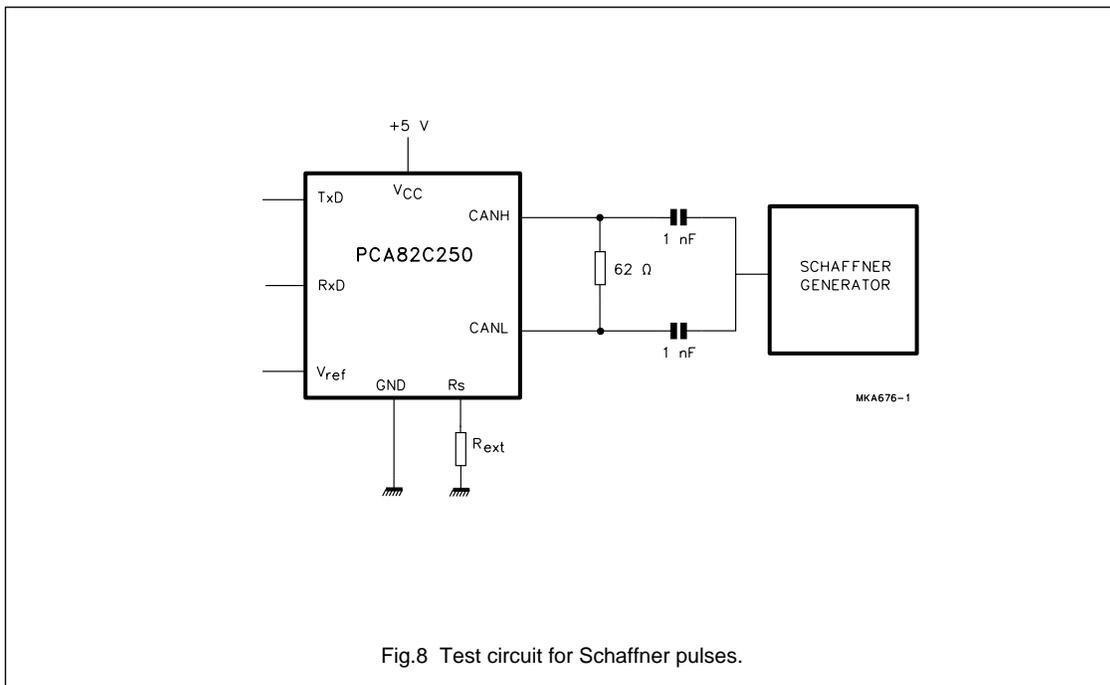
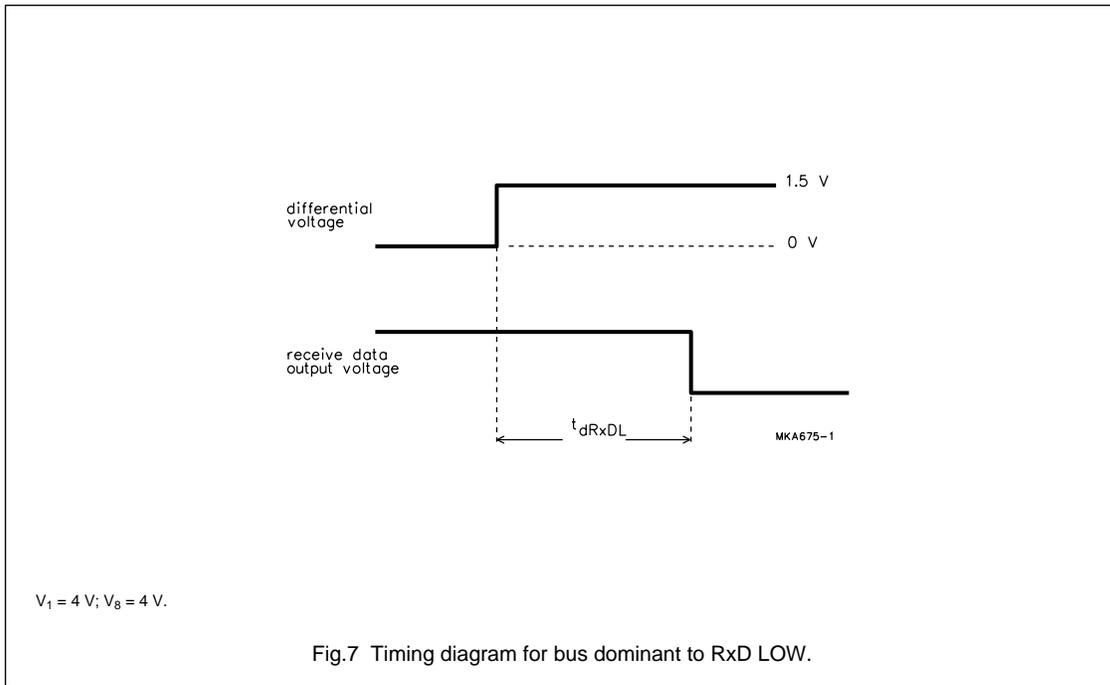


Fig.6 Timing diagram for wake-up from standby.

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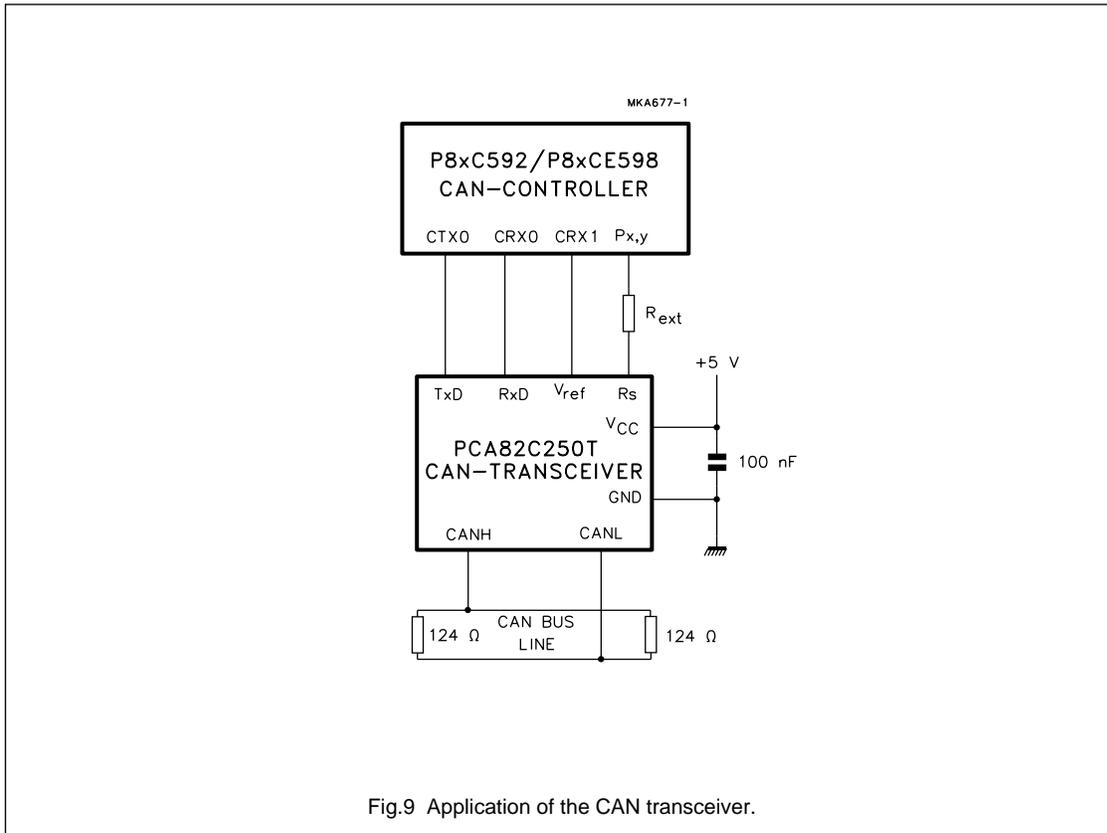
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APPLICATION INFORMATION





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INTERNAL PIN CONFIGURATION

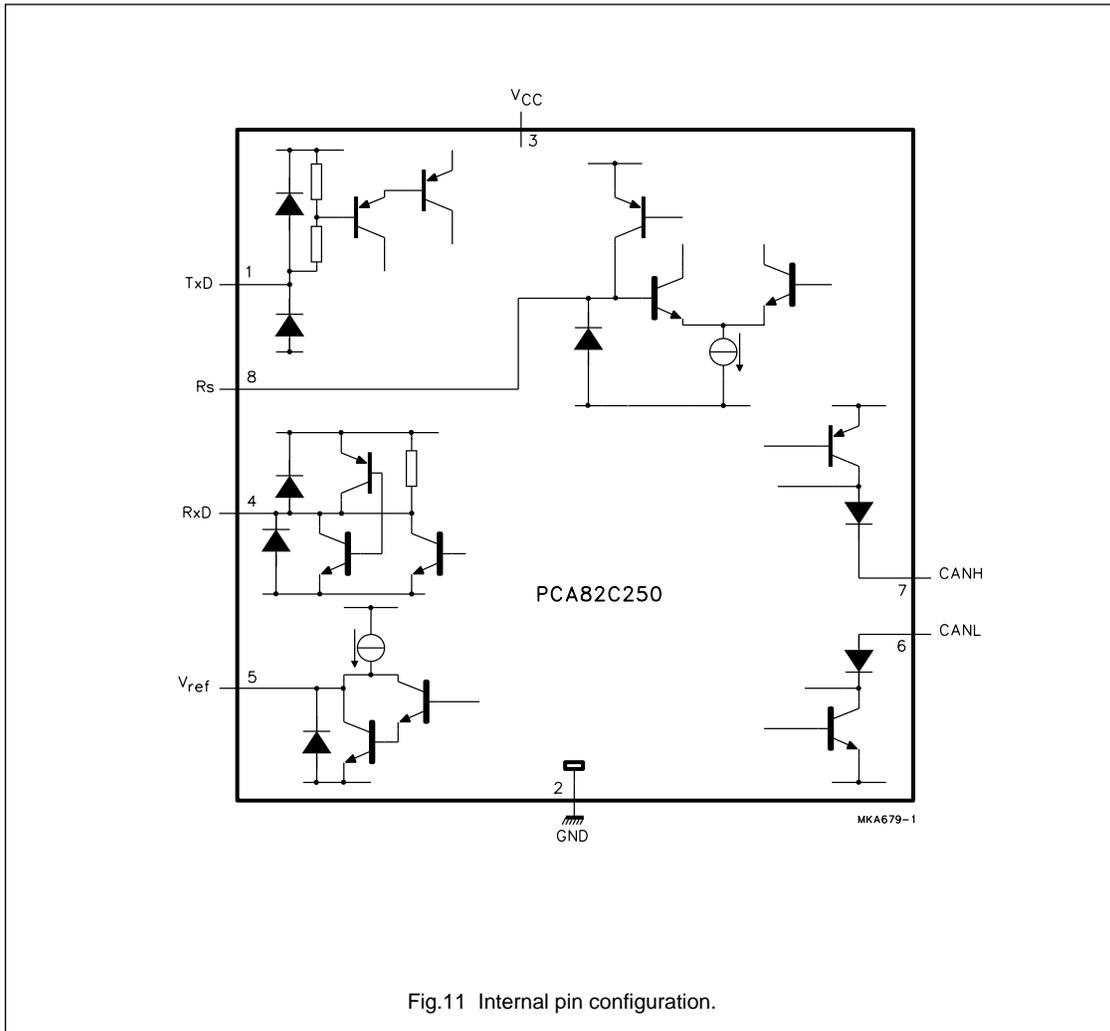


Fig.11 Internal pin configuration.